



# KAMAL NAYAN

DOB- 04-06-1996

M.Tech - Micro Electronics & VLSI Technology

B.Tech – Electronics & Communication Engineering – AKU Patna

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## EDUCATION

Bhagalpur College of Engineering (B.C.E), Bhagalpur, Bihar– AKU Patna, Bihar

**M.Tech** - *Micro Electronics & VLSI Technology*

Session: 2022-2024 -**CGPA- 8.35**

Sityog Institute Of Technology (SIT), Aurangabad, Bihar

**B.Tech** - *Electronics & Communication Engineering*

Session: 2014-2017 - **CGPA – 8.32**

Government Polytechnic Saharsa, Bihar – SBTE Bihar

**Diploma** - *Electronics Engineering*

Session: 2011-2014 -**75.37%**

G.A High School Arwal, Bihar – BSEB-Patna

**10<sup>th</sup>** -2011-**83.2%**

## SKILL

Programming Language abilities - C, C++, java, Python, HTML, CSS, Django Adaptability, Attention to detail, Creativity, Work ethics, Empathy, Leadership, Positivity, Self motivated, Multitasking.

## EXPERIENCE

Global Devices Network, New Delhi

As a Process Associate in Quality Department from **18th August 2017 to 26th March 2019.**

## COURSE/TRAINING

Embedded System and designing

Power grid corporation (132/33)

Python - Naresh it Hyderabad

## REAEARCH / PROJECTS TOPIC

Simulation Study of a Split-Channel Quad-Gate Tunnel Field-Effect Transistor.

Cell phone based remote controller for home appliances.

G.S.M based remote controller for home appliances.

## POSITIONS OF RESPONSIBILITY

Team Leader - Bal Vigyan Congress – 2010

Event Manager - Brahmaze (Cultural Fest)

## ACHIEVEMENTS

Bal Vaigyanik – 2010 (Balika Vidyapit Lakhisarai)

Alumni student of Super-30 (Ramanujam School Of Mathematics)

## EXTRA CURRICULARS

Teaching – Engg. Physics, BE, Analog, Digital, Instrumentation Engineering